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# Datasheet of adc0808 pdf

MAX5230 : 3V/5V, 12-Bit, Serial Voltage-output Dual DACs With Internal Reference. LTC1451, 5V, 0.5LSB Dnl, Micropower, Serial, Reference, SO-8. The 256R ladder network approach (Figure 1) was chosen over the conventional R/R ladder because of its inherent monotonicity, which guarantees no missing digital codes. CXA1315M : 8-bit D/A Converter Supporting With 1 2C Bus. It is also the comparator drift which has the greatest influence on the repeatability of the device. The converter is designed to give fast, accurate, and repeatable conversions over a wide range of temperatures. Easily interfaced to Microprocessors On-Chip Data Latches Monolithic Over the Entire A/D Conversion Range Interchangeable With Analog Devices AD7528 and PMI PM-7528 Fast Control Signaling for Digital Signal Processor (DSP) Applications Including Interface With TMS320 Voltage-Mode Operation CMOS Technology KEY PERFORMANCE S Resolution Linearity Error. Typical Reference and Supply Circuit 11 www.national.com Home > Integrated Circuits > A/D Converters > A/D Converters Photograph Features 8-Channel A/D Inputs with Addressable Multiplexer Easy Interface to All Microprocessors Operates Ratiometrically or with 5Vdc Output Meets TTL Requirements ADC0808 Equivalent to MM74C949 On-chip Clock Generator 0V to 5V Analog Input Voltage range with Single 5V Supply. No Zero or Full Scale Adjust Required Pin Layout Pin Description Pin Number Description 1 IN3 - Analog Input 3 2 IN4 - Analog Input 4 3 IN5 - Analog Input 5 4 IN6 - Analog Input 6 5 IN7 - Analog Input 7 6 START - Start Conversion 7 EOC - End of Conversion 8 2(3) - Tri-State Output Bit 5 9 OUT EN - Output Enable 10 CLK - Clock 11 Vcc - Positive Supply 12 Vref+ - Positive Voltage Reference Input 13 GND - Ground 14 2(7) - Tri-State Output Bit 7 15 2(6) - Tri-State Output Bit 6 16 Vref- - Voltage Reference Negative Input 17 2(8) - Tri-State Output Bit 8 18 2(4) - Tri-State Output Bit 4 19 2(3) - Tri-State Output Bit 3 20 2(2) - Tri-State Output Bit 2 21 2(1) - Tri-State Output Bit 1 22 ALE - Address Latch Enable 23 ADD C - Address Input C 24 ADD B - Address Input B 25 ADD A - Address Input A 26 INO - Analog Input 0 27 IN1 - Analog Input 1 28 IN2 - Analog Input 2 Dimensional Drawing AD7672BE03 : Lc2mod High-speed 12-bit ADC. MP7670 : 8-Channel 4 Quadrant Multiplying 8-bit Accurate Digital-to-analog Converter. With a 5 MHz Input Band-width. These features make this device ideally suited to applications from process machine control to consumer and automotive applications. 5 www.national.com 567225 FIGURE 11. TL7528EFN : Dual 8-bit Multiplying Digital-to-analog Converter. The converter features a high impedance chopper stabilized comparator, a 256R voltage divider with analog switch tree and a successive approximation register. Table 1 shows the input states for the 8-bit address lines to select any channel. The A/D converter's successive approximation register (SAR) is reset on the positive edge of the start conversion start pulse. The difference in these resistors causes the SAR character to be symmetrical with zero and full-scale points of the transition curve. AD16471CMV : 16-bit Delta-sigma 192 kS/s Analog-to-digital converter. A particular input channel is selected by using the address decoder. Eight of conversions will go low in between and 8 clock phases after the rising edge of start conversion. This is suitable for Yoke-based parallel digital VCR. The digital electronics need for external calibration for each channel are A/D converter extension, inductive temperature sensor, power driver and offset trim. The Fujitsu Lm321 is a high-resolution digital-to-video converter with a 10-bit input resolution and a bandwidth of 30MHz. The Lm321/Lm331 family of voltage-to-frequency converters are ideally suited for use in simple low-cost circuits for analog-to-digital conversion, precision frequency-to-voltage conversion, long-term integration, linear frequency modulation and demodulation, and many other applications. ADC0808 offers high speed, high accuracy, minimal temperature dependence, excellent long-term accuracy and repeatability, and consumes minimal power, < 0.8% Maximum Conversion Rate. MB40968 : 2-channel 8-bit D/A Converter. This signal is then fed through a high gain AC amplifier and has the DC level restored. TL5501CDWA : TL5501, 6-Bit ADC. 1 V TTL Digital I/O Level Low Power Consumption 200 mW Typ 5-V Single-Supply Operation Interchangeable With Fujitsu MB40576 GND DCTL VCC ANLG INPUT REFT ANLG VCC DCTL VCC The is a low-power, Serial control through I2C bus 5-channel 8-bit D/A converter Built-in 4-general-purpose I/O ports (Digital I/O) I/O can be specified to respective ports independently Selection of 8 slave addresses possible through address select pins (3 pins) Applications The IC, which The chopper-stabilized comparator converts the DC input signal into an AC signal. This technique limits the drift component of the amplifier since the drift is a DC component which is not passed by the AC amplifier. Ground Referenced Conversion System with Reference Generating VCC Supply 567226 FIGURE 12. The converter's digital outputs are positive true. Figure 4 shows a typical error curve for the ADC0808 as measured using the procedures outlined in AN-179, MAX7524 : CMOS 8-bit Buffered Multiplying DACs. For pricing, delivery, and ordering information, please contact Maxim/Dallas Direct! 1-888-629-4642, or visit Maxim's website at www.maxim-ic.com. 6-Bit Resolution Linearity Error. The first output transition occurs when the analog signal has reached +/-1 LSB and succeeding output transitions occur every 1 LSB later up to full-scale. On/Off, redundant Operation, Wait. The address is latched into the decoder on the low-to-high transition of the address latch enable signal. For any SAR type converter, n-iterations are required for an n-bit converter. The output when used as a voltage-to-frequency converter is a pulse. The 8-channel multiplexer can directly access any of 8-single-ended analog signals. The conversion is begun on the falling edge of the start-conversion pulse. The 8-bit A/D converter uses successive approximation as the conversion technique. (See AN-247 for more information.) Features ■ Easy interface to all microprocessors ■ Operates ratiometrically or with 5 VDC or analog span adjusted voltage reference ■ ■ No zero or full-scale adjust required ■ ■ 8-channel multiplexer with address logic ■ ■ 0V to VCC input range ■ ■ Outputs meet TTL voltage level specifications ■ ■ ADC0808 equivalent to MM74C949 ■ ■ ADC0809 equivalent to MM74C949-1 Key Specifications ■ ■ Resolution ■ ■ Total Unadjusted Error ■ ■ Single Supply ■ ■ Low Power ■ ■ Conversion Time 8 Bits +/-1 LSB and +/-1 LSB 5 VDC 15 mW 100 ns Block Diagram See Ordering Information © 2009 National Semiconductor Corporation 5672 567201 www.national.com Function Description MULTIPLEXER The device contains an 8-channel single-ended Analog signal multiplexer. No Preview Available ! ADC0808/ADC0809 July 8, 2009 8-Bit pP Compatible A/D Converters with 8-Channel Multiplexer General Description The ADC0808, ADC0809 data acquisition component is a monolithic CMOS device with an 8-bit analog-to-digital converter, 8-channel multiplexer and microprocessor-compatible control logic. A chopper-stabilized comparator provides the most effective method of satisfying all the converter requirements. 30 MHz Typ Analog Input Voltage Range VCC to VCC 2 V Analog Input Dynamic Range. Monotonicity is particularly important in closed loop feedback control systems. AD7813 : +2.7 V to +5.5 V, 400 KSPS 8-/10-Bit Sampling ADC. Analog Channel Selection SELECTED ANALOG CHANNEL ADDRESS LINE CBA IN0 L L1 IN1 L L H IN2 L H L IN3 L H H IN4 H L L IN5 H L H IN6 H L L IN7 H H H IN8 IN9 H H H H CONVERTER CHARACTERISTICS The Converter The heart of this single chip data acquisition system is its 8-bit analog-to-digital converter. 8-/10-Bit ADC with 2.3 s Conversion Time On-Chip Track and Hold Operating Supply Range: 5.5 V 3.6 V and 1.0% 8-Bit Parallel Interface + 2-Bit Read Power Performance Normal Operation 10.5 mW, VDD 3 V Analog Input Range; V to VREF Reference Input Range; V to VDD The is a high-speed, microprocessor-compatible, 8-/10-bit. Continuous conversion may be accomplished by tying the end-of-conversion (EOC) output to the SC input. TABLE 1. The bottom resistor and the top resistor of the ladder network in Figure 1 are not the same value as the remainder of the network. If used in this mode, an external start conversion pulse should be applied after power up. High-speed signal throughput is achieved with an innovative architecture utilizing Correlated Double Sampling (CDS), typically employed with CCD arrays, or Sample and Hold (S/H). Plug-in Modules for 19"-Subracks (3U and 6U Height) Available DC-Input Voltage Ranges 18-36,36-75,80-160 and 160-320VDC Also Models with AC-Input (see page 6) Standard Output Voltages: to 200A EMI Compliance with EN 55022 class A Safety Compliance to IEC/EN 60950 Many Options available, i.e. Powerfall Signals, Ext. The CXA1315M/P is developed 5-channel 8bit D/A converter supporting with I2C bus. The converter is partitioned into 3 major sections: the 256R ladder network, the successive approximation register, and the comparator. The successive approximation register (SAR) performs 8 iterations to approximate the input voltage. LM98714 is a fully integrated, high performance 16-Bit, 45 MSPS signal processing solution for digital color copiers, scanners, and other image processing applications. The design of the ADC0808, ADC0809 has been optimized by incorporating the most desirable aspects of several A/D conversion techniques. LM231A : Precision Voltage-to-frequency Converter (obsolete). ADC12D040 : ADC12D040 - Dual 12-Bit, 40 Msps, 600 MW A/D Converter With Internal/External Reference And Sample-and-Hold, Package: Tqfp, Pin Nb=64. Additionally, the 256R network does not cause load variations on the reference voltage. Resolution: 8-bits Linearity error: +/-0.2% (max.) Maximum conversion rate: 30 MHz (Min.) Analog output voltage range: to 5V Reference voltage output: MB40968: For 16-channel multi-plexer with common output (sample/hold port) see ADC0816 data sheet. The ADC16071 ADC16471 are 16-bit delta-sigma analog-to-digital converters using 64 c oversampling 12 288 MHz A 5th-order comb filter and a 246 tap FIR decimation filter are used to achieve an output data rate to 192 kHz. The combination of oversampling and internal digital filtering greatly reduces the external anti-alias filter requirements to a simple. A conversion in process will be interrupted by receipt of a new start conversion pulse. Figure 2 shows a typical example of a 3-bit converter. It is this section which is responsible for the ultimate accuracy of the entire converter.

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